

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	1	((FPGA (virtual adj microcontroller) (virtual adj processor) (ICE emulator)) same ((I/O adj data) (IO adj data) (I-O adj data))).clm.	US-PGPUB	OR	OFF	2008/05/28 14:00
L8	44	((ICE emulator (virtual adj (processor microcontroller))) and (I/O IO I-O)).clm.	US-PGPUB	OR	OFF	2008/05/28 14:15
L9	17	((speculative predict\$5) and (jump branch) and (I?O)).clm.	US-PGPUB	OR	OFF	2008/05/28 16:32
L10	9	((lock?step synchroniz \$5) and (ICE emulator simulator) and (I?O interrupt)).clm.	US-PGPUB	OR	ON	2008/05/28 16:35

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